

Software © 2010 Cisco Systems, Inc. All rights reserved. Cisco Confidential

XR 12000 Series Performance Route Processor
IOS XR Software © 2010 Cisco Systems, Inc. All rights reserved. Cisco Confidential

© 2010 Cisco Systems, Inc. All rights reserved. Cisco Confidential

XR 12000 Series Performance Route Processor
12000 Series Performance Route Processor, 3.9.1
© 2010 Cisco Systems, Inc. All rights reserved. Cisco Confidential

```
RP/0/0/CPU0:example#show version
Wed Dec 15 10:16:47.117 singa
```

```
Cisco IOS XR Software, Version 3.9.1[00]
Copyright (c) 2010 by Cisco Systems, Inc.
```

```
ROM: System Bootstrap, Version 12.0(20090302:133850) [rtauro-sw30346-33S 1.23dev(0.36)] DEVELOPMENT S
Copyright (c) 1994-2009 by cisco Systems, Inc.
```

```
example uptime is 26 minutes
System image file is "disk0:c12k-os-mbi-3.9.1/mbiprp-rp.vm"
```

```
cisco 12404/PRP (7457) processor with 3145728K bytes of memory.
7457 processor at 1266Mhz, Revision 1.2
```

```
1 Cisco 12000 Series Performance Route Processor
1 Cisco 12000 Series SPA Interface Processor-601/501/401
1 Cisco 12000 4 Port Gigabit Ethernet Controller (4 GigabitEthernet)
3 Management Ethernet
5 PLIM_QOS
8 FastEthernet
4 GigabitEthernet/IEEE 802.3 interface(s)
1019k bytes of non-volatile configuration memory.
982304k bytes of disk0: (Sector size 512 bytes).
62420k bytes of disk1: (Sector size 512 bytes).
65536k bytes of Flash internal SIMM (Sector size 256k).
```

!--- output truncated

```
SPA Interface Processor-601/501/401
platform 12000 Series Performance Route Processor, 3.9.1
Eng
```

```
5 © 2010 Cisco Systems, Inc. All rights reserved. Cisco Confidential
```

```
© 2010 Cisco Systems, Inc. All rights reserved. Cisco Confidential
XR 12000 Series Performance Route Processor, 3.9.1
© 2010 Cisco Systems, Inc. All rights reserved. Cisco Confidential
```

```
RP/0/0/CPU0:example#show platform
```

Mon May 9 18:40:26.100 PST

Node	Type	PLIM	State	Config State
0/0/CPU0	PRP(Active)	N/A	IOS XR RUN	PWR,NSHUT,MON
0/1/CPU0	L3LC Eng 5+	Jacket Card	IOS XR RUN	PWR,NSHUT,MON
0/1/0	SPA	SPA-8XFE-TX	READY	PWR,NSHUT
0/2/CPU0	L3LC Eng 3	GE-4	IOS XR RUN	PWR,NSHUT,MON

```
RP/0/0/CPU0:example#show diag | include SPA Interface Processor
401
```

```
RP/0/0/CPU0:example#show diag | include SPA Interface Processor
Mon May 9 18:44:23.069 PST
SLOT 1 (RP/LC 1): Cisco 12000 Series SPA Interface Processor- 401
RP/0/0/CPU0:example#
```

```
RP/0/0/CPU0:example#show diag | include SPA Interface Processor
```

Cisco IOS

```
XR 12000 Series SPA Interface Processor- 401
```

- Cisco IOS 12000 Series SPA Interface Processor
- Cisco XR 12000 Series SPA Interface Processor
- Cisco ASR 9000 Series SPA Interface Processor
- Cisco Carrier Routing System SPA Interface Processor

```
RP/0/0/CPU0:example#show diag | include SPA Interface Processor
```

è©³ç°

Cisco IOS 12000 Series SPA Interface Processor

```
XR 12000 Series SPA Interface Processor- 401
```

```
IOS XR 12000 Series SPA Interface Processor
```

```
12000 Series SPA Interface Processor
```

```
9000 Series SPA Interface Processor
```

```
IOS XR 12000 Series SPA Interface Processor
```

```
12000 Series SPA Interface Processor
```


ãf;ã,ãf£ãf¼ãfªãf¼ã,¹		
è²ã½“ã™ã,³.2.X ï½ž 3.8.Xãf™ãf¼ã,¹ã®ãfªãfªãf¼ã,¹	SMU ID	SMU å
	è²ã½“ã™ã,³.2.X ï½ž 3.8.Xãf™ãf¼ã,¹ã®ãfªãfª	
è²ã½“ã™ã,³.9.Xãf™ãf¼ã,¹ã®ãfªãfªãf¼ã,¹	SMU ID	SMU å
3.9.0	ãªã—	ã^©ç”ã¯èf½ãªSMUãĀã,ã,Šã
3.9.1	AA04896	c12k-os-mbi-3.9.1.CSCto45095
3.9.2	AA04907	c12k-os-mbi-3.9.2.CSCto45095
è²ã½“ã™ã,⁴.0.xãf™ãf¼ã,¹ã®ãfªãfªãf¼ã,¹	SMU ID	SMU å
4.0.0	ãªã—	ã^©ç”ã¯èf½ãªSMUãĀã,ã,Šã
4.0.1	AA04884	c12k-4.0.1.CSCto45095
4.0.3	Not affected	
è²ã½“⁴.1.xãf™ãf¼ã,¹ã®ãfªãfªãf¼ã,¹	SMU ID	SMU å
4.1.0	AA04976	c12k-4.1.0.CSCto45095
4.1.1	Not affected	

ã,æ£ã^©ç”ã°ã¾ã”ã...ã¼ç™ºèj”

翻訳について

シスコは世界中のユーザにそれぞれの言語でサポート コンテンツを提供するために、機械と人による翻訳を組み合わせて、本ドキュメントを翻訳しています。ただし、最高度の機械翻訳であっても、専門家による翻訳のような正確性は確保されません。シスコは、これら翻訳の正確性について法的責任を負いません。原典である英語版（リンクからアクセス可能）もあわせて参照することを推奨します。