

UCS M5 Blade DIMM Population Sticker Does Not Match Documentation

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Introduction

This document describes an issue with an old UCS M5 Blade DIMM Population Sticker that does not reflect current online documentation.

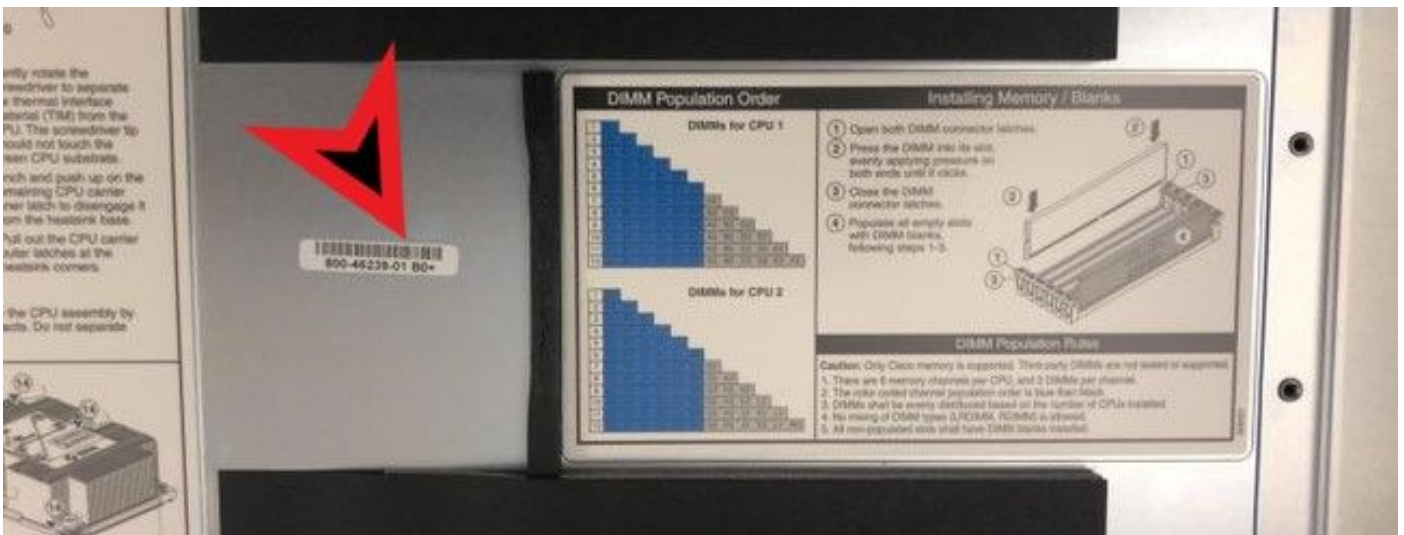
Background Information

Post-FCS testing found that the optimal configuration of the DIMM population was different than specs originally published. The original population rules do work properly, but optimal performance can be obtained by the use of the latest guidelines as published in the documentation. The sticker under the blade lid was changed under ECO EA553015 which was implemented in May 2018, and technical documentation was updated in December 2017. Approximately 15K-20K blades were shipped with the original DIMM population sticker.

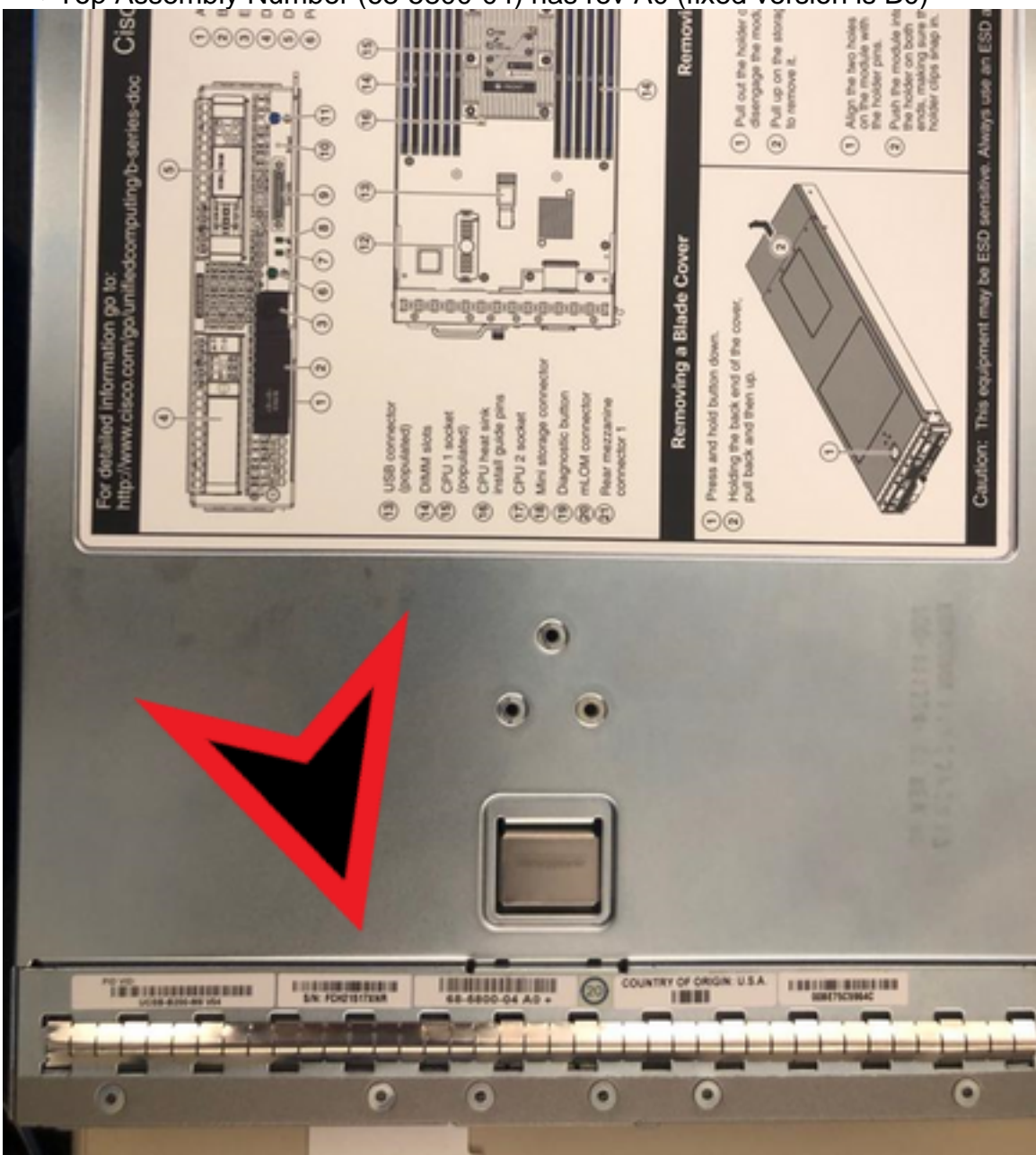
Note: Only M5 blade servers had the suboptimal stickers. Rack servers don't come with DIMM population stickers, so they are unaffected by this issue.

Affected Products:

- UCS B200 M5
- UCS B480 M5
- The top cover (800-46239-01) has rev B0 (fixed stickers have rev C0)



- Top Assembly Number (68-5800-04) has rev A0 (fixed version is B0)



Problem: UCS M5 Blade DIMM Population Sticker Does Not Match Documentation

The DIMM population sticker under the lid of UCS M5 blades does not match the information in online UCS documentation.

Both configurations are valid and supported. The old sticker should not prevent discovery or any other issues, other than it is not the optimal configuration for performance. It should be noted that the performance differential is marginal (roughly 1%).

Solution

The original DIMM population sticker is not incorrect, but customers should be directed to online documentation for the latest and most optimal configuration.

For reference, here is the **old sticker** from B200-M5:

DIMM Population Order

DIMMs for CPU 1

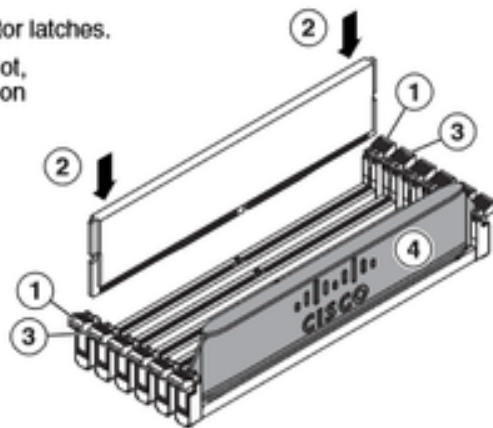
1	A1
2	A1 D1
3	A1 B1 D1
4	A1 B1 D1 E1
5	A1 B1 C1 D1 E1
6	A1 B1 C1 D1 E1 F1
7	A1 B1 C1 D1 E1 F1 A2
8	A1 B1 C1 D1 E1 F1 A2 D2
9	A1 B1 C1 D1 E1 F1 A2 B2 D2
10	A1 B1 C1 D1 E1 F1 A2 B2 D2 E2
11	A1 B1 C1 D1 E1 F1 A2 B2 C2 D2 E2
12	A1 B1 C1 D1 E1 F1 A2 B2 C2 D2 E2 F2

DIMMs for CPU 2

1	G1
2	G1 K1
3	G1 H1 K1
4	G1 H1 K1 L1
5	G1 H1 J1 K1 L1
6	G1 H1 J1 K1 L1 M1
7	G1 H1 J1 K1 L1 M1 G2
8	G1 H1 J1 K1 L1 M1 G2 K2
9	G1 H1 J1 K1 L1 M1 G2 H2 K2
10	G1 H1 J1 K1 L1 M1 G2 H2 K2 L2
11	G1 H1 J1 K1 L1 M1 G2 H2 J2 K2 L2
12	G1 H1 J1 K1 L1 M1 G2 H2 J2 K2 L2 M2

Installing Memory / Blanks

- ① Open both DIMM connector latches.
- ② Press the DIMM into its slot, evenly applying pressure on both ends until it clicks.
- ③ Close the DIMM connector latches.
- ④ Populate all empty slots with DIMM blanks, following steps 1-3.



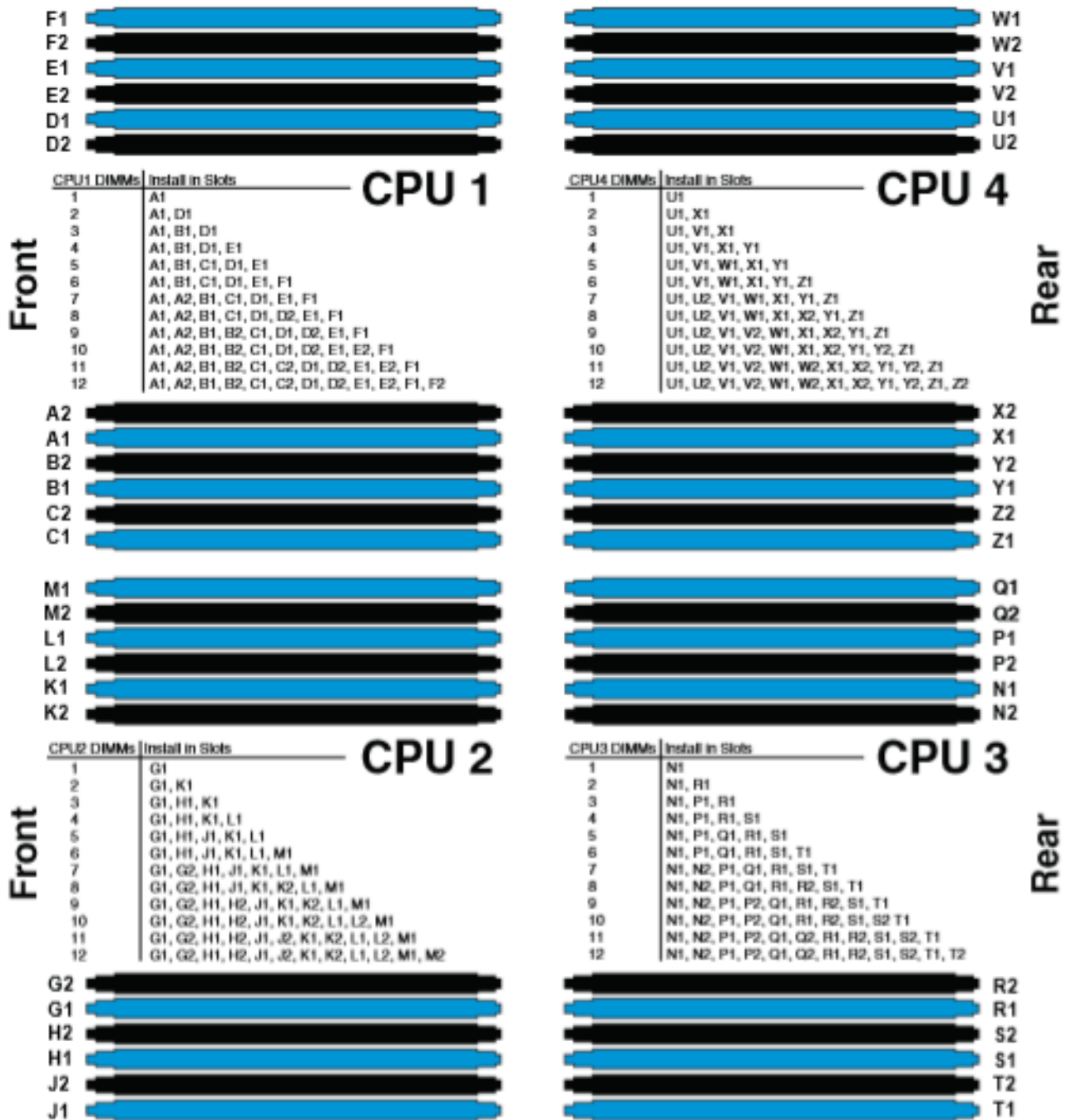
DIMM Population Rules

Caution: Only Cisco memory is supported. Third-party DIMMs are not tested or supported.

1. There are 6 memory channels per CPU, and 2 DIMMs per channel.
2. The color coded channel population order is blue then black.
3. DIMMs shall be evenly distributed based on the number of CPUs installed.
4. No mixing of DIMM types (LRDIMM, RDIMM) is allowed.
5. All non-populated slots shall have DIMM blanks installed.

For reference, here is the **old sticker** from B480-M5:

Cisco UCS B480 M5 Memory Slot Locations



DIMM Population Rules

Caution: Only Cisco memory is supported. Third-party DIMMs are not tested or supported.

There are 6 memory channels per CPU, and 2 DIMMs per channel. The color coded channel population order is blue then black. DIMMs shall be evenly distributed based on the number of CPUs installed. No mixing of DIMM types (LRDIMM, RDIMM) is allowed. All non-populated slots shall have DIMM blanks installed.

Related Information

- [Cisco UCS B200 M5 Blade Server Installation and Service Note – Memory Population Guidelines](#)
- [Cisco UCS B480 M5 Blade Server Installation and Service Note - Memory Population Guidelines](#)

- [Technical Support & Documentation - Cisco Systems](#)