

FRETTA ماظن عبتت لكاشم ليلحت (PD) يساسال

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عمدقمل

لكاشم لل قيرذجال بابسال نع شحبل او ليلحت قيفيك لوح تاداشراللا ققيثو اذه فصبي
تاهجوملا نم (NCS5500) قعومجم يلع يساسال ماظنلا يلع قدمت عملا

اهعيمجت بولطملا تالجسلا

قجردملا يساسال تالجسلا عيمجت ب مق، يساسال ماظنلا يلع دم تعي زرفي ال قس نلاب
يف حضورم وه امك، يساسال تالجسلا يللا قفاضالاب يرخاللا تالجسلا عمجب مق. هاندأ
اهي ف هبتشملا قفيظولا قحاسم وانوكم لل اق فوكل ذو، هاندأ قيعرفلا ماسق ال

اهعيمجت بولطملا يساسال تالجسلا

- show logging
- show tech ctrace
- show tech card-mgr

- show tech shelf-mgr
- Show tech ليغشت ماظن
- ين ف ضرع

سكول ةسنآل بلقلا ضربن

- لامج ةكللم ين ف ضرع

EOBC/EPC ب ةلصلا تاذتالجسلا

ةبساحملاو ضيوفتلاو ةقداصملا ةنجل ب ةصاخلا ةددحملا لئاسملا يلا عوجرلا يجرى (EOBC/EPC).

- show tech control-ethernet

جيسنلاب ةلص وذ

- xRVM نم ةينفلا ةينبلا راهظا

حئارشلا ريديم

- show tech-support sdr_mgr
- ينفل معدل تيبتت راهظا
- ينفل معدل ةينب راهظا
- show tech support fpd
- show tech support cm
- show controller fabric health
- يساسألا ماظنلا حئارش راهظا

ريديم MPA

- show tech-support mpa-mgr

نتداتقا

- ينفل معدل ةمدخ ضرع
- show tech-support alarm_mgr
- تاراذنإلا راهظا
- يودل رشؤم
- ضرعلا ةئييب

ةلاجالاو LED رشؤم لوح ليصافتلا نم ديزم يلع لوصحلل تاظا تارالا هذه يلا عوجرلا يجرى.

PD تيبتت

ةيسئيرلا (PD) فيرعتلا تافل تيبتت لكاشم يلا عوجرلا يجرى

12/16 11:11:55.290 IDLE ev_card_info_synced

ثدحلل خيراتل ةيليصفتل اتاخرم لل لاثم

```
sysadmin-vm:0_RP0# show controller card-mgr event-history detail location 0/1
Mon Dec 16 14:49:20.850 UTC+00:00
```

Card Event History for: 0/1

Card Event History as seen by Master (0/RP0)

Event buffer info:

Total number of events recorded: 14

Number of events available for display: 14

Current State: CARD_READY

EVENT #: 13 (record index = 13)

TIMESTAMP: 2019/12/16 14:46:51.116090 UTC

STATE: WAIT_CARD_INFO

EVENT: ev_card_info_synced

EVENT DESC: Card info of the remote node has been received

EVENT #: 12 (record index = 12)

TIMESTAMP: 2019/12/16 14:46:06.990465 UTC

STATE: WAIT_SYSADMIN_VM_READY

EVENT: ev_sysadmin_vm_booted

EVENT DESC: SysAdmin VM has booted

EVENT #: 11 (record index = 11)

TIMESTAMP: 2019/12/16 14:45:57.375813 UTC

STATE: HOST_OS_RUNNING

EVENT: ev_sysadmin_vm_started

EVENT DESC: SysAdmin VM has been started from host

EVENT #: 10 (record index = 10)

TIMESTAMP: 2019/12/16 14:45:39.554589 UTC

STATE: BOOTLDR_STARTED

EVENT: ev_host_os_started

EVENT DESC: Host OS has started booting

EVENT #: 9 (record index = 9)

TIMESTAMP: 2019/12/16 14:44:22.746147 UTC

STATE: CARD_POWERED_ON

EVENT: ev_bootldr_started

EVENT DESC: Bootloader on the card has started booting

EVENT #: 8 (record index = 8)

TIMESTAMP: 2019/12/16 14:44:19.142021 UTC

STATE: IOFPGA_BOOTED

EVENT: ev_dml_power_up_ok

EVENT DESC: I/O FPGA indicating power domain 1 was successfully powered up

EVENT #: 7 (record index = 7)

TIMESTAMP: 2019/12/16 14:44:12.825682 UTC

STATE: IOFPGA_RESET_CHECK

EVENT: ev_inserted

EVENT DESC: Card inserted into the chassis or I/O FPGA booted

EVENT #: 6 (record index = 6)

TIMESTAMP: 2019/12/16 14:44:12.325703 UTC

STATE: CARD_IN_RESET

EVENT: ev_removed

EVENT DESC: Card removed from chassis or I/O FPGA was power cycled

EVENT #: 5 (record index = 5)
TIMESTAMP: 2019/12/16 14:44:10.224354 UTC
STATE: PROCESS_PENDING_RESET
EVENT: if_pending_cold_reset_req

EVENT #: 4 (record index = 4)
TIMESTAMP: 2019/12/16 14:44:10.224343 UTC
STATE: SYSADMIN_VM_GOING_DOWN
EVENT: ev_host_halting_os
EVENT DESC: Host is performing halting of OS

EVENT #: 3 (record index = 3)
TIMESTAMP: 2019/12/16 14:43:50.258016 UTC
STATE: SYSADMIN_VM_GOING_DOWN
EVENT: ev_cold_reset_req
EVENT DESC: Client request to cold reset the card (I/O FPGA is also power-cycled)

EVENT #: 2 (record index = 2)
TIMESTAMP: 2019/12/16 14:43:34.275167 UTC
STATE: CARD_READY
EVENT: ev_sysadmin_vm_shutdown
EVENT DESC: SysAdmin VM shutdown operation has started

EVENT #: 1 (record index = 1)
TIMESTAMP: 2019/12/16 11:11:55.291184 UTC
STATE: OIR_INSERT_NOTIF
EVENT: if_card_local_init_done

EVENT #: 0 (record index = 0)
TIMESTAMP: 2019/12/16 11:11:55.290959 UTC
STATE: IDLE
EVENT: ev_card_info_synced
EVENT DESC: Card info of the remote node has been received

ي:ساسأل SUP و يساسأل SC لى عةظوحلمل داوكأل دعب ام لى ع لاثم

جارجأل اذه نم SUP و SC يساسأل دادعأل او ينععمل اةق اطبل لة يدامل اةحتفل مقرر لى ع فرعت

Primary SUP و 0/SC0 و Primary SC و (فرعمل دومع) 2 وه لى عفل اةحتفل مقرر 0/1 عضومل وه 0/RP0.

```
sysadmin-vm:0_RP0# show controller card-mgr inventory summary
Mon Dec 16 14:50:50.810 UTC+00:00
Card Manager Inventory Summary :
BP HW
Location Card Type ID Serial Number Ver Card State
```

```
-----
0/1 NC55-32T16Q4H-AT 2 JAE233813G2 0.302 CARD_READY
0/2 NC55-12X100GE-PROT 3 SAL1918EF3S 0.203 CARD_READY
0/FC1 NC55-5504-FC 22 JAE210600VD 0.3 CARD_READY
0/FC3 NC55-5504-FC 24 JAE210600XV 0.3 CARD_READY
0/FC5 NC55-5504-FC 26 JAE210600VX 0.3 CARD_READY
0/RP0 NC55-RP (Master) 27 SAL2044VUZT 1.0 CARD_READY
0/RP1 NC55-RP (Slave) 28 SAL1916DT8B 0.2040 CARD_READY
0/SC0 NC55-SC (Master) 29 SAL2046W07E 1.6 CARD_READY
```

0/1 طخال اةق اطبل لى ع جمربل تام لى عتال اةمئاق لى ع لوصحلل رمل اذه مدختسأ

```
sysadmin-vm:0_RP0# show controller card-mgr trace cmgr_isr location 0/SC0 | inc "slot 2" | inc
changed
```

Mon Dec 16 14:56:27.355 UTC+00:00

2019-12-16:11.14.44.916211712:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.14.44.916268544:[ISR]: POST Code for slot 22 changed to 0x54
2019-12-16:11.14.44.916295168:[ISR]: POST Code for slot 24 changed to 0x54
2019-12-16:11.14.44.916321280:[ISR]: POST Code for slot 26 changed to 0x54
2019-12-16:11.14.44.916347392:[ISR]: POST Code for slot 27 changed to 0xa0
2019-12-16:11.14.44.916373504:[ISR]: POST Code for slot 28 changed to 0xa0
2019-12-16:11.15.03.646569472:[ISR]: POST Code for slot 26 changed to 0xa0
2019-12-16:11.15.04.748022272:[ISR]: POST Code for slot 22 changed to 0xa0
2019-12-16:11.15.14.266484736:[ISR]: POST Code for slot 24 changed to 0xa0
2019-12-16:11.18.11.489846272:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.18.12.491101184:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.22.30.391535104:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.22.31.492875776:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.26.49.407702016:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.26.50.509097472:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.31.08.408430592:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.31.09.409682432:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.35.26.315185152:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.35.27.416556032:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.39.45.310315520:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.39.46.311528448:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.44.04.337517056:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.44.05.338741248:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.48.23.232193024:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.48.24.333538304:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.52.41.234022400:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.52.43.336457728:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.57.00.153080320:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.57.01.254410752:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.01.19.178457600:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.01.20.179703296:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.05.38.203790336:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.05.39.205028864:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.09.57.103055360:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.09.58.204383232:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.14.15.027237888:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.14.16.128579072:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.18.34.047417856:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.18.35.148794880:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.22.53.047706624:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.22.54.048883200:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.27.12.054199808:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.27.13.055494656:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.31.30.979380224:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.31.32.080705024:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.35.48.888316416:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.35.49.989663744:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.40.07.891782144:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.40.08.993085440:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.44.26.908366848:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.44.27.909621760:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.48.45.918578176:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.48.46.919841792:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.53.03.837281280:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.53.04.838517248:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.57.22.831639552:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.57.23.832911360:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.01.41.833031680:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.01.42.834268672:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.06.00.740024320:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.06.01.841394688:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.10.19.768019968:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.10.20.769302528:[ISR]: POST Code for slot 2 changed to 0xa0

2019-12-16:13.14.37.655355392:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.14.38.756755456:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.18.56.655229952:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.18.57.756587520:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.23.15.658801664:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.23.16.660048384:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.27.34.655034880:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.27.35.656287232:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.31.53.652897792:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.31.54.654104576:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.36.11.558914560:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.36.12.560167424:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.40.30.568370688:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.40.31.569627136:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.44.49.468186112:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.44.50.571635712:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.49.08.482063360:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.49.09.583393280:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.53.26.395422208:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.53.27.496771584:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.57.45.399475712:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.57.46.500909568:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.02.04.405213184:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.02.05.406433280:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.06.23.417884672:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.06.24.419138048:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.10.42.329566720:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.10.43.430938112:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.15.00.253901824:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.15.01.355243520:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.19.19.247721472:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.19.20.349063680:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.23.38.254869504:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.23.39.256110592:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.27.57.261724160:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.27.58.262965760:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.32.15.158858240:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.32.17.261378560:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.36.34.186439168:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.36.35.187675648:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.40.53.126042624:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.40.54.227419648:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.43.33.504493568:[ISR]: POST Code for slot 2 changed to 0xa1
2019-12-16:14.44.09.450505728:[ISR]: POST Code for slot 2 changed to 0x73
2019-12-16:14.44.18.369435136:[ISR]: POST Code for slot 2 changed to 0x1b
2019-12-16:14.44.21.973499392:[ISR]: POST Code for slot 2 changed to 0xe0
2019-12-16:14.44.45.599875072:[ISR]: POST Code for slot 2 changed to 0xe1
2019-12-16:14.45.26.660646400:[ISR]: POST Code for slot 2 changed to 0xe3
2019-12-16:14.45.28.064965632:[ISR]: POST Code for slot 2 changed to 0xe2
2019-12-16:14.45.30.167515648:[ISR]: POST Code for slot 2 changed to 0xe4
2019-12-16:14.45.33.070848000:[ISR]: POST Code for slot 2 changed to 0xe6
2019-12-16:14.45.38.777229312:[ISR]: POST Code for slot 2 changed to 0x50
2019-12-16:14.45.56.597211648:[ISR]: POST Code for slot 2 changed to 0x54
2019-12-16:14.46.06.211475968:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.46.14.720887296:[ISR]: POST Code for slot 2 changed to 0x17
2019-12-16:14.46.15.822237696:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.48.29.977753088:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.48.31.079104512:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.52.48.986328576:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.52.49.987563520:[ISR]: POST Code for slot 2 changed to 0xa0

لاصتا نود ليلحت الاء ارجال Show tech ctrace م ادخت سا

ADS مداخل لىل صتم ريغ ctrace زيمرت ك ف

1. اتانال عإل مداخل لىل لىل show tech ctrace فيشراً خسنا .
2. مداخلت ساب فيشراًل اىوتحم دي دحت عإل | **tar zxvf show tech-ctrace-admin-2019-november-06.174210.utc.tgz**
3. **cd showtech-ctrace-admin-2019-november-06.174210.utc**
4. **cmgr:** لىل دحت **card_mgr** لىل م ع راثآ زيمرت ك ف ل رمأل اذ م دختسأ .
/users/gonaidu/bin/showtech_ct_dec -d cmgr -p card_mgr
5. رآ ج م ا ي د ي س

م اذ - لىل لىل مزل ا قطنم لىل ف ا ه زيمرت ك ف م ت لىل ا لىل ج س ل ا د جوت : **عظحال م** قطنم لىل لىل دن تست ف ش ك ل ا لىل م ع ا ن ف ، **Bangalore ADS** مداخل لىل ا ه زيمرت ك ف م ت لىل ع ب ت ل ا لىل ج س و ه ج و م ل ا لىل ج س ع ن ر ا ق م ع ا ن ث ا ك ل ذ ر ك ذ ت ع ا ج ر ل ا . **IST** ل لىل مزل ا لىل اص ت ا ن و د ا ه زيمرت ك ف

سياس الال sup مداخل لىل ياس الال SC دي دحت

cmgr: لىل دحت " * " لىل ر و د " GREP " م دختسأ

```
card_mgr.0_RP0:299:2019-02-
22:07.35.38.709224844:2580:calvados/dc_common_pkg/drivers/card_mgr/src/card_mgr_main.c:1539:main
:cmgr_main:MAIN_HW_ARB_RESULT:[MAIN]: HW Arbitration Result = Master Role
card_mgr.0_SC0:96:2019-04-
06:19.04.34.500975616:1976:calvados/dc_common_pkg/drivers/card_mgr/src/card_mgr_main.c:1539:main
:cmgr_main:MAIN_HW_ARB_RESULT:[MAIN]: HW Arbitration Result = Master Role
```

سياس الال SC نم ع قح الال داوك الال دادر س

ريغ ت | **LC 0/1: GREP "slot 2" card_mgr.0_SC0** لىل ر و م ر لىل ل و ص ح ل ل ر م أل ا اذ م دختسأ ب و ر ج

```
2019-04-
06:19.04.34.759844864:1976:calvados/dc_common_pkg/drivers/card_mgr/src/card_mgr_interrupt.c:256:
iofpga_check_card_post_code_change:cmgr_isr:ISR_CARD_POST_CODE_CHANGED:[ISR]: POST Code for slot
2 changed to 0xa0
```

سياس الال RP رظن ه ج و نم ا د ا ح الال ا ط و ف ح م :

grep rcapi_notify_card_post_code_cb card_mgr 0_rp0 | GREP "0V1"

```
2019-04-
06:19.04.40.665774834:2589:calvados/dc_common_pkg/drivers/card_mgr/src/card_mgr_rack_service.c:1
919:cmgr_rack_notify_card_post_code_change_cb:cmgr_rack_capi:RCAPI_NOTIFY_CARD_POST_CODE_CB:[RAC
K CAPI]: 0/1 - CAPI cmgr_rack_notify_card_post_code_change_cb, client card_mgr
(PID=1976,hdl=0x7f7c880ff728,slot=29)
```

زرف ال ا ه و ي ر ا ن ي س ل ا ث م

- **ع ق ا ط ب ل ا ع ر ا د ا ل (CPU) لىل ر م ل ا ع ج ل ا م ل ا د ح و ع ج ل ا م د ع ا س م :**

- LC Reload-Potenza 10C ىلع ةحيرشلا رهظت ال

؟يروتاس ةعومجم عاشن او ريرحتو بحس كنكمي فيك

دشي نأ بجي نيأ نم سندنهمل فرعي نأ بجي .اعم ةدوجوم لسالس ةدع كانه نوكت نأ نكمي مق sysadmin linux ةبلاطم نم ،بسانم يضارتفا رادصا/يروتاس طخ بيترت ةفرعمل .رظنملا رمألا اذه ليغشتب

```
cat /etc/build-info.txt
```

ةقيرطالا هذبه ةعاضبلا ىلع لصحت فوس

```
### Thirdparty Information
```

```
SDK arm /auto/exr-yocto/SDK/WRL7/Fretta/REL0109/arm/kvm-host-arm-sdk.tgz
```

```
SDK x86_64 /auto/exr-yocto/SDK/WRL7/Fretta/REL0109/x86_64/kvm-host-x86_64-sdk.tgz
```

```
Refpoint = thirdparty/opensource/release@tp-main/289
```

```
Hostname      : calcium-99.cisco.com
```

```
Workspace     : /nobackup/hetsoi/satori-wrl7.release.20191209/target-n9000-gdb
```

```
Source Base   : ssh://wwwin-git-sjc-2/git/thinstack/satori.git
```

```
Devline      : cisco-xr-wr7
```

```
Devline Ver   : f53915539d9ca49d3dedec0882ee4eb12a408956
```

```
Devline Type  : GIT Repository
```

```
Here Devline 'cisco-xr-wr7' should be used.
```

```
Before pulling the view, setup your environment
```

كبا صاخلا GIT نيوكت فلم في دوجوم لادالا اذه نأ نم دكأت 2. ةوطخال

[قبيطت]

nowarn = ءاضيبل ةفاسملا

ريغت = لهاجتلا ةفاسم

طاشن ي أب مابقلا لبق كتئي ب دادعإ رمألا اذه مدختسأ 3. ةوطخل

`/auto/exr-yocto/tools/scripts/set_yocto_env` ردملا

ضرعلا بحسل رمألا اذه مدختسأ 4. ةوطخل

ال ب `ACME -sb ios_ena -dev cisco-xr-wr7 -plat` بحس

كترشق يف يلاتل رمألا ليغشت ب مق 5. ةوطخل

CDPATH نيغت اغل

kernel انا ب رمأ

رمألا اذه ليغشت ب مقو satori لي دلى لقتنا 1. ةوطخل

`scripts/xr/build-release.sh -f`

لى Cisco نم ةصاخلا حيحصتلا مزح قبطي هنإف، حاجن ب Kernel اشنإ درجم ب 2. ةوطخل تاميلعتلا لىل ريسلل راسملا اذه يف ةرفوتم حبصت يتلا ةفلتخمل زمرلا و `h.c`. تافل مةيجمربلا.

`satori/target-n9000-gdb/bitbake_build/tmp/work/n9000_gdb-wrs-linux/`

كماظن يف كلذ سكعل انه مئاد ريغت ي أب موقت نأ ضررتفملا نم سئل: **ةطخال** يلاتل انا بلى يف هقوق ةباتكلا متيسو و TMP لوكوتورب عقوم وه اذه نأل يئانثلا تاميلعتلا ريسل عقوملا اذه يف ةدوجوملا ةيجمربلا تاميلعتلا مادختسا بحى انه **patch**. فلم ةشقانم تمت **patch**. فلم اشنإو ةيجمربلا

يف ةدوجوم KLM ل ةيجمربلا تاميلعتلا نإف، ةيجمربلا تاميلعتلا عقوم رظن ةهجو نم نيراسم

• حيحصتلا جماربو "ةيجمربلا تاميلعتلا لىل يشملا" ةزيم اشنإل
`satori/target-n9000-gdb/bitbake_build/tmp/work/n9000_gdb-wrs-linux/`

• `sdk`/ةيئانث تارادصا يف مدختست `h.c`. لىل عتافل مةيجمربلا
`satori/meta-cisco-nxos/recipes-kernel/`

نيليلدىل لىل لصحتس، هالع راسملا لىل

A. Cisco-klm → ةباتكلا لىل KLM ةفاك اذه يطيغي

b. cisco-klm-zermatt → fretta ةمظنا يف طقف اهمادختسا متي يتلا KLM ةفاك اذه يطيغي
klm_iofpga لثم ةباتكلا

اهعيمجتو ةفلتخمل KLMS لىل عتاريفيغتلا ارجا متي فيك

اذه لىل لاقنتالا لىل عتاريفيغتلا ارجا دعى ةياغللا اطي سب ارمأ `klm_iofpga` لىل عتاريفيغتلا ارجا دعى

ك.مه ي يذال h. وأ c. فلم لى لع تاريخيغت عارجا ي ف عدبلاو راسملا

```
satori/target-n9000-gdb/bitbake_build/tmp/work/n9000_gdb-wrs-linux/cisco-klm-zermatt/0.1-r0/klm_iofpga
```

جاتحت ،هالعا حضورم وه امك .عيشلا ضعب بعص وه فىرخالا تاودالا لك لى لع تاريخيغت عارجا ام
فلم عاشن اة فيك) patch. فلم عاشن او ،تاريخيغتلا عارجا او ،TMP عقوم لى للاقتنالا لى
ي ف ديدجال patch. فلملل لاخدا ب مقو ،ددحم عقوم لى ل patch. فلم خسنا .(انه حضورم patch.
عانبلا ادباو bb. فلم

تاريخيغتلا عارجالا تاوطخلا لى لع روثعلا عارجلا

ي ف تامكللكلا لك دجت .تاريخيغتلا عارجا ديرت ثيح KLM ل ددحملا لى لى للاقتنالا 1. ةوطخلا
عقوملا اذه

```
satori/target-n9000-gdb/bitbake_build/tmp/work/n9000_gdb-wrs-linux/cisco-klm/0.1-r0
```

انه ،ظحال .patch. فلم عاشن انكمي ثيح ب ،تاريخيغتلا لمعل فاحللا اءا مدختسا 2. ةوطخلا
ححصتلا فلم عاشن ل فاحللا اءا لمعتسي نأ جاتحت تنأ ك لذل ،لمعي ال ACME diff

فاحللا اءال راعتسملا مسالا دادع 3. ةوطخلا

```
cd satori/target-n9000-gdb/bitbake_build/tmp/work/n9000_gdb-wrs-linux/cisco-klm/0.1-r0
```

```
quilt=/nobackup/rpanday/kernel-wr7/satori/target-n9000-gdb/bitbake_build/tmp/sysroot/x86_64-linux/usr/bin/quilt
```

مساب ديدجال patch. فلم صيصخت رمأ رادصاب مق —> patch_file.patch ل ديدجال رمأ رادصاب مق
patch_file.patch.

ريرتلل زهجو فاحللا سدكم لى لعأ ي ف دجوي patch_file.patch نأ رمألا اذه انربخي —> quilt top

مت ي تلا تاريخيغتلا طاقتلال فاحللا هي جوتب مق —> klm_obfl/obfl_dc3.c فاحللا ريحت
ازهجو احوتم فم obfl_dc3.c نوكي ،رمألا اذه ليغشت درجم ب klm_obfl/obfl_dc3.c ي ف اهوارجا
جورخلاو ةباتكلاب مق ،تاريخيغتلا نم اءاتنالا دع ب .ريرتلل

klm_obfl/obfl_dc3.c هريحت مت ي ذللا فلملا نم diff رمألا رمألا اذه ذخأ ي —> quilt refresh
VI ي ف فلملا حتفا ،كلذ نم ققحتلل patch_file.patch ي ف هعضي و

اذه لى لهسنا ، patch patch patch patch_file.patch حصتلا فلم عاشن ا درجم ب 4. ةوطخلا
راسملا

```
cp patches/patch_file.patch /nobackup/rpanday/kernel-wr7/satori/meta-cisco-nxos/recipes-kernel/cisco-klm/files
```

عقوملا اذه ي ف bb. فلم دجوي .اذه ديدجال patch. فلمل bb. فلم ي ف لاخدا عارجا ب مق 5. ةوطخلا

```
/nobackup/rpanday/kernel-wr7/satori/meta-cisco-nxos/recipes-kernel/cisco-klm/cisco-klm_0.1.bb
```

HW و FPGA نم ةتباثلا Fretta تاقاطب تافصاوم

PID تافصاوم IOFPGA تافصاوم عون ةسدنهل مسا

ةزهجالا ةقاطبالا

NCS-5502-SE	تامريز	تباث 2RU	·راي عم EDCS-1026647 ·راي عم EDCS-1516467 ·راي عم EDCS-1193041 ·راي عم EDCS-1527505 ·راي عم EDCS-1527506
NCS-5501-SE	ون يروت-MX	تباث 1RU	·راي عم EDCS-1497433
NCS-5502-SE- proto	وت ورب-تامريز		·راي عم EDCS-1516467
NCS-5502	رآ يس تامريز	تباث 2RU	·راي عم EDCS-1515475 CPU iOFPGA FS: EDCS-1026647 MIFPGA - EDCS - 1193041 IOFPGA - EDCS - 1541805 MIFPGA - EDCS - 1541804
NCS-5501	وهيات	تباث 1RU	·راي عم EDCS-1530044
NCS-5501-A2-SE	لي فبرتن يو		
NCS-5501-A1-SE	لس الكدل وأ		
NCS-5501-A1	NCS-55A1-36H-S لس الكدل وأ		
NCS-55A1-36H- SE-S	يس لس الكدل وأ	تباث 1RU	·زارطال EDCS-1568105 ·زارطال EDCS-11402862 ·زارطال EDCS-11556985 ·زارطال EDCS-1568105 ·زارطال EDCS-11402862
NCS-55A1-36H-S	لس الكدل وأ	تباث 1RU	·زارطال EDCS-1563746 ·زارطال EDCS-11402862
N540-X- 24Z8Q2C-M	قودنص ريغ ني تروت يمسر		
N540-24Z8Q2C- M	رآ ن تروت		
N540X-ACC-SYS	ن تروت-16g-cr		
N540-ACC-SYS	ن تروت-16g		
NCS-5501- HD	N540-24Z8Q2C- M		
NCS-5501-A3	رآ ن تروت NCS-55A1-24H		
NCS-55A1-24H	كيباب كيباب	تباث	·راي عم EDCS-·راي عم EDCS-

		1RU	11415948	1568105 EDCS- 1026647 EDCS- 1568940 EDCS- 11632621 EDCS- 11632622 EDCS- 11632621 EDCS- 11632622
NCS-55A2-MOD- SE-S	وت تيپ وت وت وت وت ماك	تباث 2RU	EDCS- 11601538	EDCS- 11632621 EDCS- 11632622 EDCS- 11632621 EDCS- 11632622
NC55A2-MOD- SE-H-S	وت تيپ مادخت ساب TCAM و CC	تباث 2RU	EDCS- 11601538	EDCS- 11632621 EDCS- 11632622 EDCS- 11632621 EDCS- 11632622
NCS-55A2-MOD- S	Peyto NonSE C- Temp (TCAM)	تباث 2RU	EDCS- 11601538	EDCS- 11632621 EDCS- 11632622 EDCS- 11632621 EDCS- 11632622
NCS-55A2-MOD- HD-S	وت تيپ ي نودب ماك	تباث 2RU	EDCS- 11601538	EDCS- 11632621 EDCS- 11632622 EDCS- 11632621 EDCS- 11632622
NCS-55A2-MOD- HX-S	وت تيپ نودب TCAM م ITEMP CC	تباث 2RU	EDCS- 11601538	EDCS- 11632621 EDCS- 11632622 EDCS- 13259042 EDCS- 15599029 EDCS- 15676955 EDCS- 13259042
NCS-55A1- 48Q6H	وت تيپ ي تسورفيپ	تباث 1RU	EDCS- 12914104	EDCS- 15599029 EDCS- 15676955 EDCS- 13259042
NCS-55A1- 24Q6H-S	وت تيپ رايس ونپروت	تباث 1RU	EDCS- 12909672	EDCS- 15599029 EDCS- 15676955
NCS-55A1- 24Q6H-SS	وت تيپ رايس ونپروت			

ةمچرتل هذه لوج

ةللأل تاي نقتل نم ةومچم مادختساب دن تسمل اذہ Cisco تچرت
ملاعلاء انءمچي فني مدختسمل معدى وتحم مي دقتل ةيرشبل او
امك ةقيقد نوك تنل ةللأل ةمچرت لصف أن ةظحال مچري. ةصاخل مهتبل ب
Cisco يلخت. فرتحم مچرت مامدقي يتل ةيفارتحال ةمچرتل عم لالحل وه
ىلإ أمئاد ةوچرلاب ي صؤت وتامچرتل هذه ةقد نع اهتيل وئسم Cisco
Systems (رفوتم طبارل) ي لصلأل يزي لچنل دن تسمل